

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Changming Jin et al.

Docket Number: TI-26111

Serial No.: 09/178,249

Examiner: G. Lee

Filed: 10/23/1998

Art Unit: 2825

Integrated Circuit Dielectric and Method



Assistant Commissioner
for Patents
Washington, DC 20231

NOTICE OF APPEAL

MAILING CERTIFICATE	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 today.	
<i>Gracia Sansom</i> Gracia Sansom	<i>9-27-01</i> Date

Dear Sir:

Applicant hereby appeals to the Board of Appeals from the decision of the Primary Examiner dated **07/02/2001** all rejected claims.

The item(s) checked below are appropriate:

1. ☐ An extension of time to respond to the final rejection
☐ was granted on
☐ is requested for month(s).
2. ☐ A timely response to the final rejection has been filed.
3. ☒ Fee \$310.00:
☐ Not required (Fee paid in prior appeal).
☒ The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to the Deposit Account of Texas Instruments Incorporated, Account No. 20-0668. An original and one copy of this sheet are enclosed.

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Signature [Rule 191(b)]

10/03/2001 SLUANG1 00000010 200668 09178249

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Post Office Address
(to which correspondence is to be sent)

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